

INFORMATION DISCLOSURE STATEMENT

Applicant	:	DeBoer and Agarwal
App. No.	:	Unknown
Filed	:	Filed Herewith
For	:	METHOD AND STRUCTURE FOR REDUCING CONTACT ASPECT RATIOS
Examiner	:	Unknown
Group Art Unit	:	unknown

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing 20 references that are of record in U.S. patent application No. 09/997,660, filed November 28, 2001, which is the parent of this Continuation application, and is relied upon for an earlier filing date under 35 U.S.C. § 120. Copies of the references are not submitted pursuant to 37 C.F.R. § 1.98(d).

This Information Disclosure Statement is being filed with an RCE or within three months of the filing date of this application and no fee is required in accordance with 37 C.F.R. § 1.97(b)(1), (b)(2), or (b)(4).

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: November 17, 2003

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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (USE SEVERAL SHEETS IF NECESSARY)	ATTY. DOCKET NO. MICRON.080C1C1	APPLICATION NO. Unknown
	APPLICANT DeBoer and Agarwal	
	FILING DATE Herewith	GROUP Unknown

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	1.	5,057,888	10/15/91	Fazan et al.			
	2.	5,250,457	10/5/93	Dennison			
	3.	5,292,677	3/8/94	Dennison			
	4.	5,338,700	8/16/94	Dennison et al.			
	5.	5,401,681	3/28/95	Dennison et al.			
	6.	5,488,011	1/30/96	Figura et al.			
	7.	5,554,557	9/10/96	Koh			
	8.	5,940,714	8/17/99	Lee et al.			
	9.	5,998,257	12/7/99	Lane et al.			
	10	6,046,093	4/4/00	DeBoer et al.			
	11	6,083,803	7/4/00	Fischer et al.			
	12	6,083,831	7/4/00	Dennison			
	13	6,174,767 B1	1/16/01	Chi			
	14	6,184,079	2/6/01	Lee			
	15	6,204,143	03/2001	Roberts et al.			
	16	6,221,711	4/24/01	Roberts et al.			
	17	6,222,222 B1	4/24/01	DeBoer et al.			
	18	6,365,453	04/2002	DeBoer et al.			
	19	6,417,065	07/2002	Wu et al.			

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
	20	Sakao et al. "A Capacitor-Over-Bit-Line (COB) Cell with Hemispherical-Grain Storage Node For 64Mb DRAMs, "IEDM, VOL 90, San Francisco 12./9-12/12/90, pp 27.3.1-27.3.4

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EXAMINER	DATE CONSIDERED
*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.	